## **Electronic Information Disclosure Statement**

## DOUBLE GATE TRENCH TRANSISTOR

Application:

Confirmation:

Applicant(s): James Adkisson

Docket

BUR919990299

Number:

**Group Art Unit:** 

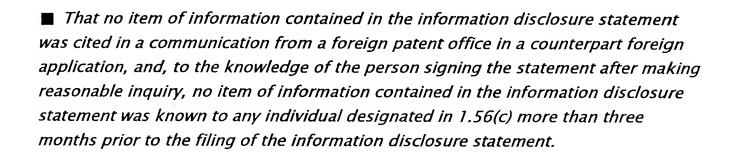
Examiner:

Thomas J. Magee

search string:

(5773331 or 5583368 or 4965218 or 4849376 or 4847212 or 4701423 or 4048646 or

4622569 ).pn.



## **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Citation No.	Patent Number	Date	Bar Code	Patentee	Class	Subclass
伽	P01	5773331	1998-06-30		Solomon et al.	438	164
	P02	5583368	1996-12-10		Kenney	257	621
	P03	4965218	1990-10-23	ETATAN DIKAKANIN KANTANIN KANTANIN KANTANIN	Geissberger et al.	437	41



4	m	P04	4849376	1989-07-18		Balzan et al.	437	228
		P05	4847212	1989-07-11		Balzan et al.	437	41
	n	P06	4701423	1987-10-20		Szluk	437	57
F	M	P07	4048646	1977-09-13		Ogawa et al.	357	15
	N	P08	4622569	1986-11-11		Lade et al.	357	23.4

## Signature

Examiner Name	Date
Homas Mage	3/15/03